RAJA Portability Suite Update

P3HPC Virtual Forum

September 1-2, 2020

Rich Hornung, LLNL, with contributions from many others
Our open source software tools enable applications to run on HPC systems in a performance portable way

**RAJA**: C++ kernel execution abstractions
- Enable single-source application source code insulated from hardware and programming model details

**CHAI**: C++ array abstractions
- Automate data copies giving look and feel of unified memory

**Umpire**: Memory management
- High performance memory operations, such as pool allocations, with native C++, C, Fortran APIs

**camp**: C++ metaprogramming facilities
- Focused on HPC compiler compatibility

- RAJA: [https://github.com/LLNL/RAJA](https://github.com/LLNL/RAJA)
- CHAI: [https://github.com/LLNL/CHAI](https://github.com/LLNL/CHAI)
- Umpire: [https://github.com/LLNL/Umpire](https://github.com/LLNL/Umpire)
- camp: [https://github.com/LLNL/camp](https://github.com/LLNL/camp)
Applications rely on these tools to run on current systems and evolve to future platforms (Frontier, Aurora, El Capitan, ...)

- LLNL WSC production codes use the tools in a variety of combinations
  - Applications: multiple rad-hydro, deterministic & MC transport, ICF, engineering
  - Support libraries: EOS, TN burn, HE chemistry, sliding contact, AMR, mesh-to-mesh linking

- Other LLNL program apps: NIF VBL, magnetic confined plasma simulations, ...

- The tools are part of the ECP ST ecosystem used in ECP apps and libraries, such as
  - SW4 (AD-EQSIM)
  - GEOS (AD-Subsurface)
  - ExaSGD (AD)
  - LLNL ATDM
  - SUNDIALS, DevilRay (ST-Alpine), MFEM (CEED co-design)

LLNL institutionally-funded RADIUSS effort facilitates adoption across the lab.
Notable RAJA accomplishments since last year’s meeting

- 5 releases on GitHub

- New released features include:
  - Initial **HIP back-end** support for all RAJA features (AMD GPUs)
  - Initial support for **asynchronous kernel execution** (also works with Umpire and CHAI)
  - Work groups (**fuse many small GPU kernels**) into one kernel launch
  - A “Multi-view” abstraction (enabling multiple arrays to share indexing arithmetic)
  - Multiple **sort algorithms**
  - Expanded GPU capabilities and performance improvements
    - Block-direct, thread, warp, bitmask execution policies, atomic local array type for atomics in GPU shared mem
  - **Dynamic “plug-in” support** and integration with Kokkos performance tools (J. Hynes – summer)

- Notable external engagements
  - Tutorials at ATPESC 2019, ECP Annual Meeting 2020; ExaSGD, SW4 Hack-a-thons
  - Created DESUL (DoE Standard Utility Library) org and repo ([https://github.com/desul/desul](https://github.com/desul/desul))
    - Collaboration with Sandia and Oak Ridge, eventually other C++ projects
  - Working with Marvell on RAJA Perf Suite optimization for ARM processors
New RAJA support for GPU streams enables asynchronous execution (M. Davis, T. Scogland, D. Beckingsale)

```cpp
chai::ManagedArray<double> a1(N);  chai::ManagedArray<double> a2(N);

RAJA::resource::Cuda cuda1;
RAJA::resource::Cuda cuda2;

auto event1 = forall<cuda_exec_async>(&cuda1, RangeSegment(0, N),
                                         [=] RAJA_DEVICE (int i) { a1[i] = ... } );

auto event2 = forall<cuda_exec_async>(&cuda2, RangeSegment(0, N),
                                         [=] RAJA_DEVICE (int i) { a2[i] = ... } );

cuda1.wait_on(&event2);  // or event2.wait();

forall<cuda_exec_async>(&cuda1, RangeSegment(0, N),
                         [=] RAJA_DEVICE (int i) { a1[i] *= a2[i]; } );

forall<seq_exec>(RangeSegment(0, N),
                  [=] (int i) { printf("a1[%d] = %f \n", i, a1[i]); } );
```

Resource objects passed to RAJA execution methods.

RAJA execution methods return event objects that can be queried or waited on.

CHAI arrays know which resources are using them so host-device data transfers can happen as soon as data is ready.

Plan to explore similar extensions to other programming model back-ends.
RAJA “work groups” enable many small kernels to be packed into one GPU launch to reduce overhead (J. Burmark, P. Robinson)

Key use case: Packing/unpacking halo data for MPI comm**

*Multiple data copies performed in one kernel launch*

**In 2 production apps, this yields 5-15% overall performance boost**
Notable Umpire accomplishments since last year’s meeting

- 6 releases on GitHub

- New released features include:
  - Support for **HIP back-end** (AMD GPUs)
  - Support for **OpenMP target back-end** (alternative mechanism for managing data)
  - Support for **OneAPI back-end** (SYCL support for Intel GPUs)
  - **Zero-byte allocations** supported as a native concept (track allocations to specific allocator)
  - Asynchronous copy and memset operations
    - Works with RAJA and CHAI to overlap data transfer and compute operations
  - Improved “replay” support
    - Binary capability to handle large (100G+) replay files
    - Additional memory operations (copy, etc.) are now recorded also
  - Added **multi-device support** for CUDA, HIP, and OpenMP target
Umpire accomplishments, ctd...

- New released features include:
  - **Backtrace support** (e.g., track allocations in app codes)
  - Additional memory operations, such as prefetch for NVIDIA GPUs
  - **NV memory file allocation support** (A. Perez – summer)

- Notable external engagements
  - Tutorial at ECP Annual Meeting 2020; ExaSGD, SW4 Hack-a-thons
Notable CHAI accomplishments in the past year

- 3 releases on GitHub
- New released features include:
  - Support for **AMD HIP** programming model (AMD GPUs)
  - Use of Umpire’s fast Judy array-based map implementation to store pointer records
  - Transition to unified logging across Umpire and CHAI
  - Adoption of **RAJA “plugin” mechanism** which allows CHAI to automatically integrate with RAJA when libraries are built together
  - "Managed pointer" simplifies use of **virtual class hierarchies** across memory spaces
  - **Eviction capability** is easy to use and enables integrated apps to run larger problems by avoiding pool fragmentation
CHAI ‘managed_ptr’ solution simplifies use of virtual class hierarchies across host/device memories (P. Robinson, A. Dayton)

- New CHAI ‘managed_ptr’ enables such code to be run on GPUs without a major refactor

```cpp
void overlay( Shape* shape, double* mesh_data ) {
    chai::managed_ptr< Shape > mgd_shape = shape->makeManaged();
    RAJA::forall< cuda_exec > ( ... { ... } mgd_shape->processData(mesh_data[i]);
}

mgd_shape.free();
}
```

- This requires methods to clone objects and host-device decorations on constructors

```cpp
chai::managed_ptr< Shape > Sphere::makeManaged( ) { ... }
__host__ __device__ Sphere::Sphere( ... ) { ... }
```
CHAI eviction capability yields significant memory and execution time benefits for multiphysics apps (P. Robinson, A. Dayton)

```
chai::getResourceMgr() ->
evict(chai::CPU, chai::GPU);
```

Max available size for B
Other features in development
RAJA vector interface can encapsulate vector intrinsics so code will SIMD-ize transparently (A. Kunen)

using Vec_type = RAJA::StreamVector<double, 2>;
using Vecidx_type = RAJA::VectorIndex<int, Vec_type>;

RAJA::kernel< KernelPolicy<...vector_exec<...>...>> (segments,
    [=] (int m, int d, int g, Vecidx_type z) {
        phi(m, g, z) += L(m, d) * psi(d, g, z);
    });
RAJA “Teams” prototype being developed in collaboration with the MFEM team (A. Vargas)

- Potentially more flexible than RAJA::kernel interface
  - Simpler run time policy selection (e.g., CPU or GPU)
  - New nested loop patterns
- Opens up hierarchical parallelism opportunities
- Aligns well with MFEM algorithm structures macro layers

```
int N = ...;
launch<launch_policy>(select_cpu_or_gpu,
    Resources(Teams(N), Threads(N)),
    [=] RAJA_HOST_DEVICE(LaunchContext ctx) {
        loop<teams_pol>(ctx, RangeSegment(0, N), [&](int r) {
            loop<threads_pol>(ctx, RangeSegment(r, N), [&](int c) {
                M(r, c) = ...;
            });
        }
    });
```
Our Argonne ECP collaborators have made substantial progress toward a RAJA SYCL back-end (B. Homerding, et al.)

- ECP SW4 app and much of RAJA Perf Suite working
- Performance looks promising
- Working through memory management issues
- Work remains to support all RAJA features and resolve performance issues

Mean Runtime Report (sec.)

<table>
<thead>
<tr>
<th>Kernel</th>
<th>Base_SYCL</th>
<th>RAJA_SYCL</th>
</tr>
</thead>
<tbody>
<tr>
<td>Basic_DAXPY</td>
<td>0.263559</td>
<td>0.247529</td>
</tr>
<tr>
<td>Basic_IF_QUAD</td>
<td>0.192065</td>
<td>0.204579</td>
</tr>
<tr>
<td>Basic_INIT3</td>
<td>0.407615</td>
<td>0.371487</td>
</tr>
<tr>
<td>Basic_MULADDSUB</td>
<td>0.284180</td>
<td>0.261049</td>
</tr>
<tr>
<td>Basic_NESTED_INIT</td>
<td>0.468795</td>
<td>0.491765</td>
</tr>
<tr>
<td>Lcals_DIFF_PREDICT</td>
<td>0.495185</td>
<td>0.488606</td>
</tr>
<tr>
<td>Lcals_EOS</td>
<td>0.382110</td>
<td>0.458804</td>
</tr>
<tr>
<td>Lcals_FIRST_DIFF</td>
<td>0.865176</td>
<td>0.796497</td>
</tr>
<tr>
<td>Lcals_GEN_LIN_RECUR</td>
<td>0.716076</td>
<td>0.658505</td>
</tr>
<tr>
<td>Lcals_HYDRO_1D</td>
<td>0.688161</td>
<td>0.627595</td>
</tr>
<tr>
<td>Lcals_HYDRO_2D</td>
<td>1.251182</td>
<td>1.122538</td>
</tr>
<tr>
<td>Lcals_INT_Predict</td>
<td>0.580458</td>
<td>0.627150</td>
</tr>
<tr>
<td>Lcals_PLANCKIAN</td>
<td>0.044052</td>
<td>0.050743</td>
</tr>
<tr>
<td>Lcals_TRIDIAG_ELIM</td>
<td>0.584795</td>
<td>0.670069</td>
</tr>
<tr>
<td>Apps_DEL_DOT_VEC_2D</td>
<td>0.138256</td>
<td>0.201736</td>
</tr>
<tr>
<td>Apps_ENERGY</td>
<td>0.664540</td>
<td>0.599901</td>
</tr>
<tr>
<td>Apps_FIR</td>
<td>0.278217</td>
<td>0.413339</td>
</tr>
<tr>
<td>Apps_PRESSURE</td>
<td>0.877004</td>
<td>0.827753</td>
</tr>
<tr>
<td>Apps_VOL3D</td>
<td>0.110188</td>
<td>0.194256</td>
</tr>
</tbody>
</table>
Acknowledgements

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  - Rich Hornung (PL)
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- camp
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  - David Beckingsale

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